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ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM  
10055138

FILING DATE  
01/23/2002

CLASS  
2524

SUBCLASS  
38 186

GAU  
1778  
2844

EXAMINER  
STEINF

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428/641

\*\*CONTINUING DATA VERIFIED:

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\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☐ no  
35 USC 119 conditions met ☐ yes ☐ no  
Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

YOR920010499US1 (14710)

TITLE Method of creating high-quality relaxed SiGe-on-insulator for strained Si CMOS applications

U.S. DEPT. OF COM. / PAT. & TM. PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED

Assistant Examiner

CLAIMS

Total Claims

10

ISSUE FEE

Amount Due

Date Paid

DRAWING

Sheet Draw

Fig. Draw

Print Fee

Primary Examiner

PREPARED FOR ISSUE

Application Examiner



TERMINAL

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